

ISSUE CLASSIFICATION	
Class	Subclass

FILED UNDER 35 U.S.C. 371

PATENT NUMBER

U.S. UTILITY Patent Application

<p><i>per</i></p> <p>O.I.P.E.</p> <p>SCANNED <i>KEZ</i> Q.A. <i>QHT</i></p>	PATENT DATE
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APPLICATION NO. 09/889260	CONT/PRIOR D F	CLASS 327 327	SUBCLASS 156	ART UNIT 2815 2816	EXAMINER M. Nguyen
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APPLICANTS: Siegfried Hart
 Heinz Werker

TITLE: Electronic phase-locking loop (pll)

PTO-2040
 12/89

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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